

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2802	327/538,540,541,543.ccls.	US-PGPUB; USPAT	OR	OFF	2005/07/30 12:40
L2	866	365/227.ccls.	US-PGPUB; USPAT	OR	OFF	2005/07/30 12:41
L3	12	(1 or 2) and (resistor with fus\$4 with parallel)	US-PGPUB; USPAT	OR	OFF	2005/07/30 12:45
L4	361	(1 or 2) and (resistor with parallel)	US-PGPUB; USPAT	OR	OFF	2005/07/30 12:45
L5	349	4 not 3	US-PGPUB; USPAT	OR	OFF	2005/07/30 12:45
L6	38	5 and "internal circuit"	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:03
L7	2	(1 or 2) and "load adjustment"	US-PGPUB; USPAT	OR	OFF	2005/07/30 12:51
L8	76	(1 or 2) and (load near5 adjust\$4)	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:03
L9	67	8 not (3 or 6 or 7)	US-PGPUB; USPAT	OR	OFF	2005/07/30 12:57
L10	89	(1 or 2) and (load\$3 near5 parallel)	US-PGPUB; USPAT	OR	OFF	2005/07/30 12:57
L11	0	10 not (3 or 6 or 7 or 10)	US-PGPUB; USPAT	OR	OFF	2005/07/30 12:57
L12	78	10 not (3 or 6 or 7 or 9)	US-PGPUB; USPAT	OR	OFF	2005/07/30 12:58
L13	3452	(1 or 2) not (3 or 6 or 7 or 9 or 12)	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:03
L14	279	13 and "internal circuit"	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:03
L15	41	14 and (current near5 adjust\$4)	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:09
L16	2930	13 and (transistor\$1 or MOS or MOSFET or FET or PMOS or NMOS or PNP or NPN or bipolar)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:48
L17	1651	13 and ((P or N) adj (MOS or MOSFET or FET or channel or type))	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:49
L18	233	(16 or 17) and 14 not 15	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:11
L19	597	(16 or 17) and (load near3 current\$1)	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:12
L20	57	18 and 19	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:21
L21	96	18 and resistor\$1 not 20	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:28

L22	80	18 not (20 or 21)	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:48
L23	70	14 and 19	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:48
L24	0	23 not (15 or 18)	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:49
L25	527	19 not (15 or 20)	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:50
L26	99	25 and (vin\$1 or "v.sub.int" or "v.sub. in"\$1 or vccvint or "vcc.sub.int" or "v. sub.ccint" or vddint or "vdd.sub.int" or "v.sub.ddint" or intvcc or intvdd)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:51
L27	428	25 not 26	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:58
L28	14	27 and (diver\$4 near5 current\$1)	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:59
L29	146	27 and (switch\$3 near3 current\$1)	US-PGPUB; USPAT	OR	OFF	2005/07/30 13:59
L30	139	29 not 28	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:06
L31	221	27 and gate not (28 or 30)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:06
L32	428	27 and load	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:06
L33	221	31 and 32	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:07
L34	221	33 and current	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:08
L35	167	34 and resistor\$1	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:08
L36	14	34 and fus\$4	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:09
L37	157	35 not 36	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:13
L38	54	34 not 35	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:16
L39	207	32 not 31	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:16
L40	207	39 and current\$1	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:16
L41	67	40 and internal	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:18
L42	140	40 not 41	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:22
L43	2651	(1 or 2) not (3 or 6 or 7 or 9 or 12 or 15 or 18 or 19)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:43

L44	810	43 and internal	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:23
L45	1805	43 and gate	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:23
L46	2152	43 and current	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:24
L47	592	44 and 45 and 46	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:24
L48	212	47 and load	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:24
L49	205	48 and (16 or 17)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:26
L50	146	49 and switch\$3	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:29
L51	59	49 not 50	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:30
L52	380	47 not 48	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:31
L53	164	52 and ((adjust\$3 or chang\$3 or vary\$3 or vari\$2 or control\$4) near3 current\$1)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:35
L54	216	52 not 53	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:35
L55	13	54 and "operation signal"	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:36
L56	88	54 and resistor\$1 not 55	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:38
L57	115	54 not (55 or 56)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:42
L58	2325	323/312,313,314,315,317.ccls.	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:43
L59	2106	58 not (3 or 6 or 7 or 9 or 12 or 15 or 18 or 19 or 47)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:44
L60	439	59 and internal	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:44
L61	1273	59 and gate	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:44
L62	2068	59 and current	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:44
L63	978	59 and load	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:44
L64	161	60 and 61 and 62 and 63	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:48
L65	331	61 and (load near3 current\$1)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:48

L66	230	65 not 64	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:48
L67	2253	58 and (transistor\$1 or MOS or MOSFET or FET or PMOS or NMOS or PNP or NPN or bipolar)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:49
L68	1057	58 and ((P or N) adj (MOS or MOSFET or FET or channel or type))	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:49
L69	229	66 and (67 or 68)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:49
L70	201	69 and resistor\$1	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:49
L71	28	69 not 70	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:50
L72	321	58 and (vin\$1 or "v.sub.int" or "v.sub.in"\$1 or vccvint or "vcc.sub.int" or "v.sub.ccint" or vddint or "vdd.sub.int" or "v.sub.ddint" or intvcc or intvdd)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:51
L73	26	70 and 72 not 71	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:52
L74	175	70 not (71 or 73)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:52
L75	100	74 and parallel	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:56
L76	75	74 not 75	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:58
L77	293	72 not (71 or 73)	US-PGPUB; USPAT	OR	OFF	2005/07/30 14:59
L78	143	77 and parallel	US-PGPUB; USPAT	OR	OFF	2005/07/30 15:02
L79	150	77 not 78	US-PGPUB; USPAT	OR	OFF	2005/07/30 15:02